

1. Unbalanced voltage effects on a single phase multilevel inverter due to control strategies  
 Aguillon-Garcia, J.; Fernandez-Nava, J.M.; Banuelos-Sanchez, P.;  
[Telecommunications Energy Conference, 2004. INTELEC 2004. 26th Annual International](#)  
 19-23 Sept. 2004 Page(s):140 - 145  
 Digital Object Identifier 10.1109/INTLEC.2004.1401457

[AbstractPlus](#)

| Full Text:

[PDF\(664](#)

KB) [IEEE](#)

[CNR](#)

[Rights and](#)

[Permissions](#)

2. A New Simplified Multilevel Inverter Topology for DC-AC Conversion  
 Ceglia, G.; Guzman, V.; Sanchez, C.; Ibanez, F.; Walter, J.; Gimenez, M.I.;  
[Power Electronics, IEEE Transactions on](#)  
 Volume 21, Issue 5, Sept. 2006 Page(s):1311 - 1319  
 Digital Object Identifier 10.1109/TPEL.2006.880303

[AbstractPlus](#) | Full Text: [PDF\(936 KB\)](#) [IEEE](#) [CNR](#)

[Rights and Permissions](#)

3. Data converters performance at extreme temperatures  
 Ramesham, R.; Kumar, N.; Mao, J.; Keymeulen, D.; Zebulum, R.S.; Stoica, A.;  
[Aerospace Conference, 2006 IEEE](#)  
 4-11 March 2006 Page(s):12 pp.  
 Digital Object Identifier 10.1109/AERO.2006.1655972

[AbstractPlus](#) | Full Text: [PDF\(4352 KB\)](#) [IEEE](#) [CNR](#)

[Rights and Permissions](#)

4. 14th IEEE International Conference on Electronics, Circuits and Systems  
[Electronics, Circuits and Systems, 2007. ICECS 2007. 14th IEEE International Conference on](#)  
 11-14 Dec. 2007 Page(s):1 - 1  
 Digital Object Identifier 10.1109/ICECS.2007.4510893

[AbstractPlus](#) | Full Text: [PDF\(68 KB\)](#) [IEEE](#) [CNR](#)

[Rights and Permissions](#)

5. Comparing floating-point and logarithmic number representations for reconfigurable acceleration  
 Haohuan Fu; Mencer, O.; Luk, W.;  
[Field Programmable Technology, 2006. FPT 2006. IEEE International Conference on](#)  
 Dec. 2006 Page(s):337 - 340  
 Digital Object Identifier 10.1109/FPT.2006.270342

[AbstractPlus](#) | Full Text: [PDF\(114 KB\)](#) [IEEE](#) [CNR](#)

[Rights and Permissions](#)

6. Reconfigurable particle filter design using dataflow structure translation  
 Sangjin Hong; Xiaoyao Liang; Djuric, P.M.;  
[Signal Processing Systems, 2004. SIPS 2004. IEEE Workshop on](#)  
 2004 Page(s):325 - 330  
 Digital Object Identifier 10.1109/SIPS.2004.1363071

[AbstractPlus](#) | Full Text: [PDF\(360 KB\)](#) [IEEE](#) [CNR](#)

[Rights and Permissions](#)

7. Delay insensitive NCL reconfigurable logic  
 Meekins, K.; Ferguson, D.; Basta, M.;  
[Aerospace Conference Proceedings, 2002. IEEE](#)  
 Volume 4, 2002 Page(s):4-1961 - 4-1966 vol.4  
 Digital Object Identifier 10.1109/AERO.2002.1036908

[AbstractPlus](#) | Full Text: [PDF\(728 KB\)](#) [IEEE](#) [CNR](#)

[Rights and Permissions](#)

8. Field programmable gate array based three-phase PWM inverter  
Mekhilef, S.; Rahim, N.A.;  
[Transmission and Distribution Conference and Exhibition 2002, Asia Pacific, IEEE/PES](#)  
Volume 3, 6-10 Oct. 2002 Page(s):1953 - 1958 vol.3  
Digital Object Identifier 10.1109/TDC.2002.1177758  
[AbstractPlus](#) | Full Text: [PDF](#)(410 KB) IEEE CNF  
[Rights and Permissions](#)
9. A reconfigurable pipelined data converter  
Mortezapour, S.; Lee, E.K.F.;  
[Circuits and Systems, 2001, ISCAS 2001, The 2001 IEEE International Symposium on](#)  
Volume 4, 6-9 May 2001 Page(s):314 - 317 vol. 4  
Digital Object Identifier 10.1109/ISCAS.2001.922235  
[AbstractPlus](#) | Full Text: [PDF](#)(448 KB) IEEE CNF  
[Rights and Permissions](#)
10. The COMPASS data acquisition system  
Fischer, H.; Franz, J.; Grunemaier, A.; Heinsius, F.H.; Hoffmann, M.; Karstens, F.; Kastaun, W.; Konigsmann, K.; Niebuhr, M.;  
Risken, R.; Schmidt, T.; Schmitt, H.; Schweimler, A.; Hodenberg, M.V.; Urban, H.J.;  
[Nuclear Science, IEEE Transactions on](#)  
Volume 49, [Issue 2](#), Part 1, April 2002 Page(s):443 - 447  
Digital Object Identifier 10.1109/TNS.2002.1003770  
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(217 KB) IEEE JNL  
[Rights and Permissions](#)
11. Statistical Analysis and Design of HARP FPGAs  
Wang, G.; Sivaswamy, S.; Ababei, C.; Bazargan, K.; Kastner, R.; Bozorgzadeh, E.;  
[Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on](#)  
Volume 25, [Issue 10](#), Oct. 2006 Page(s):2088 - 2102  
Digital Object Identifier 10.1109/TCAD.2005.859485  
[AbstractPlus](#) | Full Text: [PDF](#)(944 KB) IEEE JNL  
[Rights and Permissions](#)
12. Toward on-chip JIT synthesis on Xilinx VirtexII-Pro FPGAs  
Bergeron, Etienne; Feeley, Marc; David, Jean Pierre;  
[Circuits and Systems, 2007, NEWCAS 2007, IEEE Northeast Workshop on](#)  
5-8 Aug. 2007 Page(s):642 - 645  
Digital Object Identifier 10.1109/NEWCAS.2007.4487978  
[AbstractPlus](#) | Full Text: [PDF](#)(190 KB) IEEE CNF  
[Rights and Permissions](#)
13. Extreme Temperature Electronics based on Self-Adaptive System using Field Programmable Gate Array  
Keymeulen, D.; Zebulum, R.; Rajeshuni, R.; Stoica, A.; Katkooi, S.; Graves, S.; Novak, F.; Antill, C.;  
[Aerospace Conference, 2007, IEEE](#)  
3-10 March 2007 Page(s):1 - 6  
Digital Object Identifier 10.1109/AERO.2007.352652  
[AbstractPlus](#) | Full Text: [PDF](#)(1226 KB) IEEE CNF  
[Rights and Permissions](#)
14. Self-Adaptive System Based on Field Programmable Gate Array for Extreme Temperature Electronics  
Keymeulen, D.; Zebulum, R.; Rajeshuni, R.; Stoica, A.; Katkooi, S.; Graves, S.; Novak, F.; Antill, C.;  
[Adaptive Hardware and Systems, 2006, AHS 2006, First NASA/ESA Conference on](#)  
15-18 June 2006 Page(s):296 - 300  
Digital Object Identifier 10.1109/AHS.2006.64  
[AbstractPlus](#) | Full Text: [PDF](#)(608 KB) IEEE CNF  
[Rights and Permissions](#)
15. An optimal architecture for a DDC  
Bijlsma, T.; Wolkotte, P.T.; Smit, G.J.M.;  
[Parallel and Distributed Processing Symposium, 2006, IPDPS 2006, 20th International](#)

25-29 April 2006 Page(s):8 pp.

Digital Object Identifier 10.1109/IPDPS.2006.1639440

[AbstractPlus](#) | Full Text: [PDF\(400 KB\)](#) [IEEE CNF](#)

[Rights and Permissions](#)



16. Configurable computing architectures for wireless and software defined radio - a FPGA prototyping experience using high level design-tool-chains

Blackner, A.; Aibl, S.; Scherr, W.;

[System-on-Chip, 2004. Proceedings. 2004 International Symposium on](#)

16-18 Nov. 2004 Page(s):111 - 116

Digital Object Identifier 10.1109/ISSOC.2004.1411162

[AbstractPlus](#) | Full Text: [PDF\(737 KB\)](#) [IEEE CNF](#)

[Rights and Permissions](#)



17. Hardware optimized direct digital frequency synthesizer architecture with 60 dBc spectral purity

Langlois, J.M.P.; Al-Khalili, D.;

[Circuits and Systems, 2002. ISCAS 2002. IEEE International Symposium on](#)

Volume 5, 26-29 May 2002 Page(s):V-361 - V-364 vol.5

Digital Object Identifier 10.1109/ISCAS.2002.1010715

[AbstractPlus](#) | Full Text: [PDF\(436 KB\)](#) [IEEE CNF](#)

[Rights and Permissions](#)



18. Design and Implementation of a Field Programmable Analogue Array

Bratt, A.; Macbeth, I.;

[Field-Programmable Gate Arrays, 1996. FPGA '96. Proceedings of the 1996 ACM Fourth International Symposium on](#)

1996 Page(s):88 - 93

[AbstractPlus](#) | Full Text: [PDF\(192 KB\)](#) [IEEE CNF](#)

[Rights and Permissions](#)



19. The design of Mars lander cameras for Mars Pathfinder, Mars Surveyor '98 and Mars Surveyor '01

Reynolds, R.O.; Smith, P.H.; Bell, L.S.; Keller, H.U.;

[Instrumentation and Measurement, IEEE Transactions on](#)

Volume 50, Issue 1, Feb. 2001 Page(s):63 - 71

Digital Object Identifier 10.1109/19.903879

[AbstractPlus](#) | [References](#) | Full Text: [PDF\(200 KB\)](#) [IEEE JNL](#)

[Rights and Permissions](#)



20. Design of a real-time digital Simulator for a D-STATCOM system

Dinavahi, V.; Iravani, R.; Bonert, R.;

[Industrial Electronics, IEEE Transactions on](#)

Volume 51, Issue 5, Oct. 2004 Page(s):1001 - 1008

Digital Object Identifier 10.1109/TIE.2004.834954

[AbstractPlus](#) | [References](#) | Full Text: [PDF\(776 KB\)](#) [IEEE JNL](#)

[Rights and Permissions](#)



21. IEEE International Symposium on Circuits and Systems (ISCAS) (IEEE Cat. No. 05CH37618)

[Circuits and Systems, 2005. ISCAS 2005. IEEE International Symposium on](#)

23-26 May 2005

Digital Object Identifier 10.1109/ISCAS.2005.1464505

Full Text: [PDF\(608 KB\)](#) [IEEE CNF](#)

[Rights and Permissions](#)



22. Intelligence in embedded controls - a case study

Muthuraman, R.; Fajebe, A.; Commuri, S.;

[Region 5 Conference: Annual Technical and Leadership Workshop, 2004](#)

2 April 2004 Page(s):125 - 130

Digital Object Identifier 10.1109/REG5.2004.1300180

[AbstractPlus](#) | Full Text: [PDF\(1788 KB\)](#) [IEEE CNF](#)

[Rights and Permissions](#)